

The diagram illustrates a CCD output circuit. A horizontal transfer register (4) is shown at the bottom, with two clock inputs labeled  $\phi H1$  and  $\phi H2$ . The register is divided into sections (1, 2, 3) and contains vertical columns of squares representing pixels. The output of the register is connected to a buffer (5) and then to the CCD output (6). The output is labeled "CCD OUTPUT".

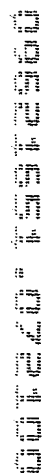
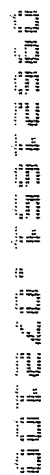
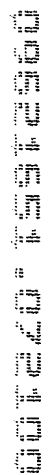
[illegible][illegible][illegible]

FIG. 3A

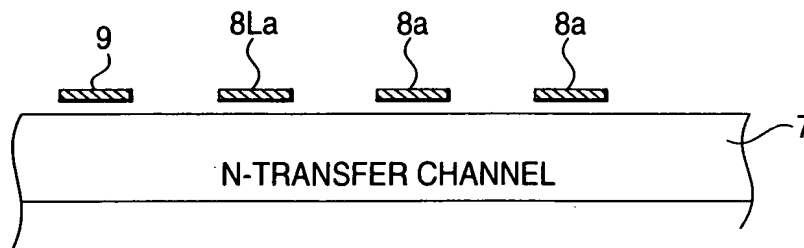


FIG. 3B

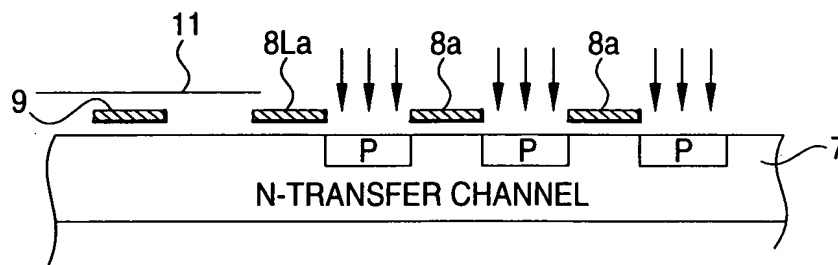


FIG. 3C

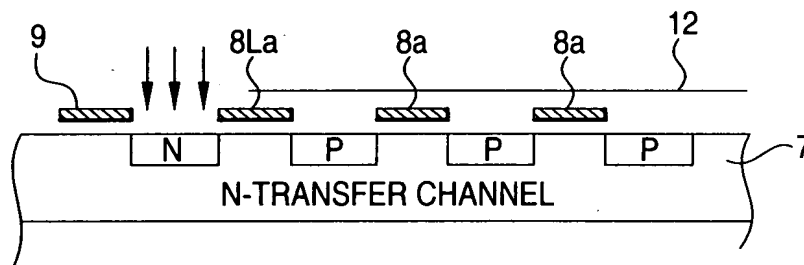


FIG. 3D

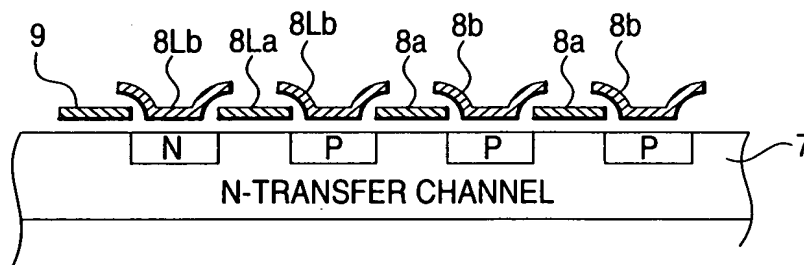


FIG. 3E

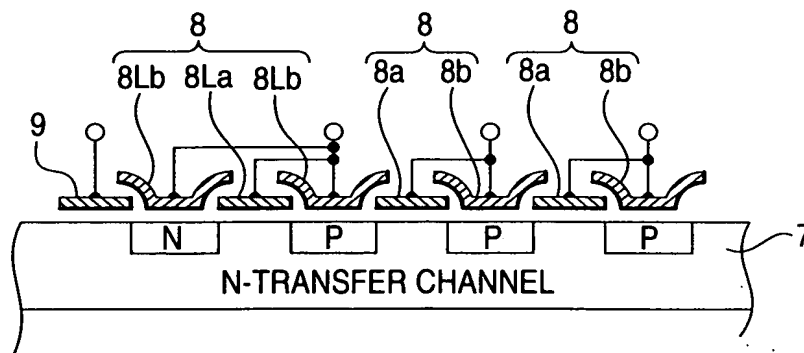


FIG. 4A

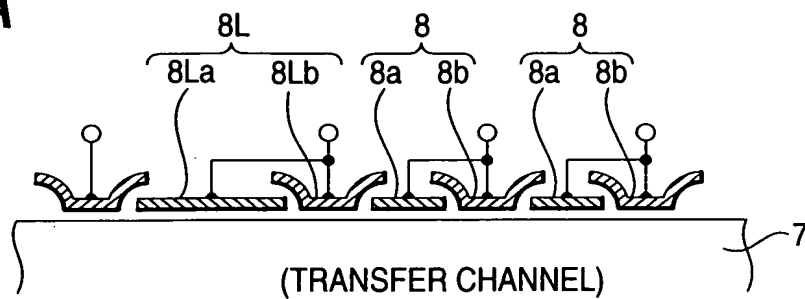


FIG. 4B

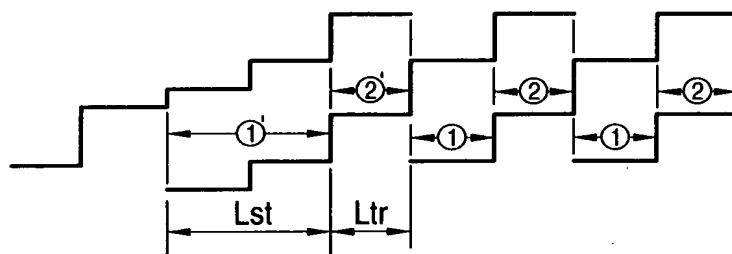


FIG. 4C

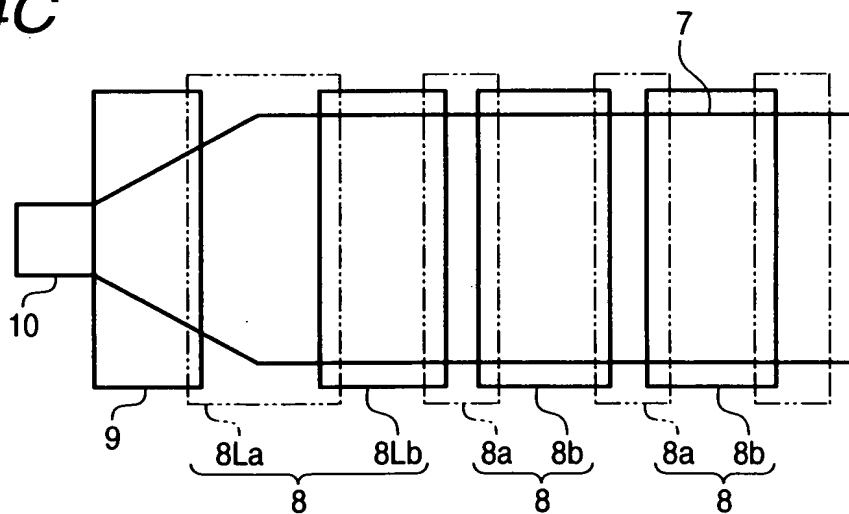


FIG. 5A

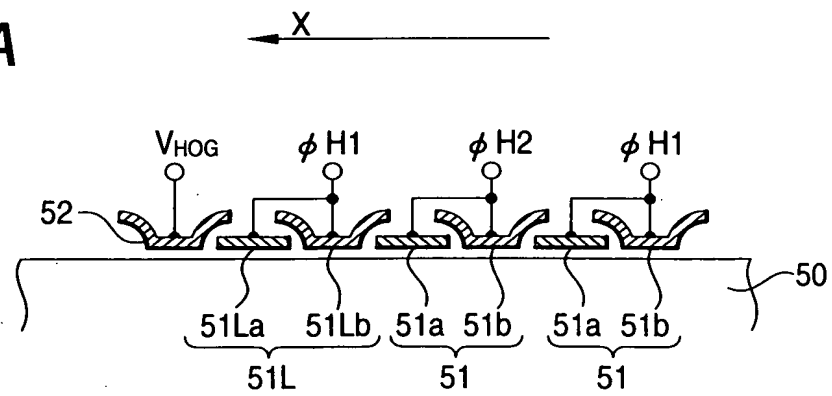


FIG. 5B

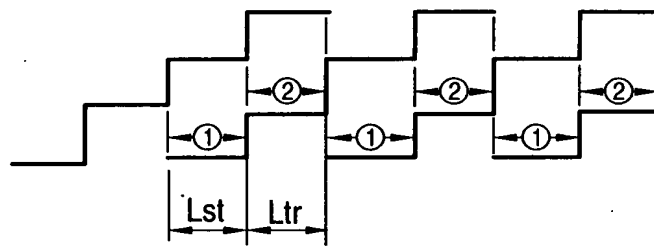


FIG. 5C

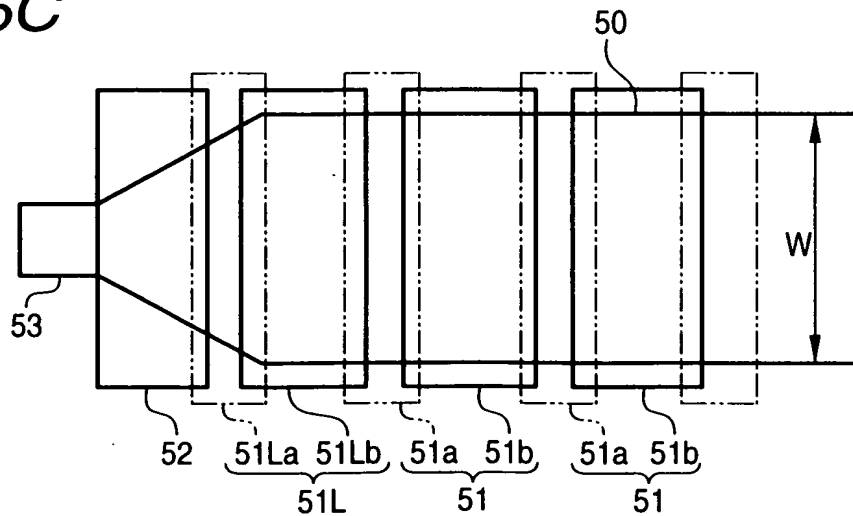


FIG. 6A

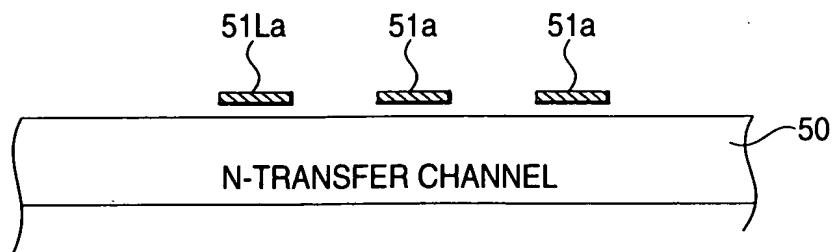


FIG. 6B

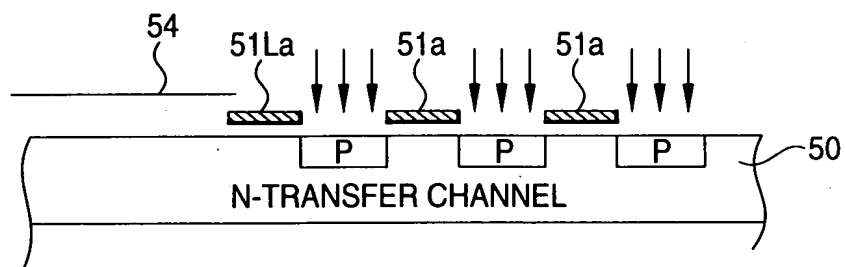


FIG. 6C

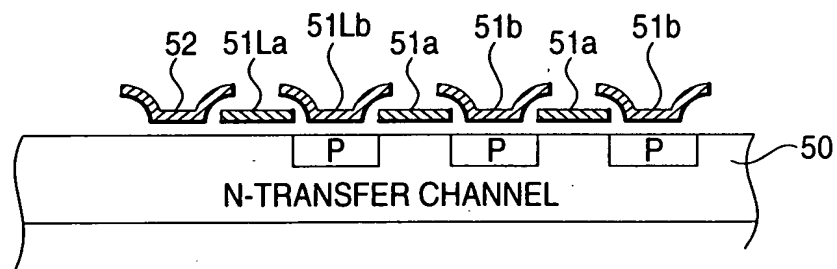


FIG. 6D

